IAPZOREC'C PCT/PTO 06 JUNT 20196/08a ' Sheet of ' 1 ATTY, DOCKET NO. SERIAL NO. INFORMATION DISCLOSURE Unknown U. 581786 CITATION 3670-66 APPLICANT GEVORGIAN ET AL. (Use several sheets if necessary) FILING DATE TC/A U 4125 Unknown June 6, 2006 U.S. PATENT DOCUMENTS *EXAMINER FILING DATE NAME SUBCLASS IF APPROPRIATE INITIA DOCUMENT NUMBER DATE 9/2000 Donnelly et al. 6,125,157 /J.P./ 2/2002 6.350.335 Hampel et al. FOREIGN PATENT DOCUMENTS TRANSLATION DOCUMENT DATE COUNTRY CLASS SUBCLASS YES NO /J.P./ wo 99/43036 8/1999 ABST OTHER DOCUMENTS (including Author, Title, Date, Pertinent pages, etc.) International Search Report for PCT/SE2003/001918 dated 2 August 2004. Eto et al., A 333MHz, 20mW, 18ps resolution digital DLL using current-controlled delay with parallel variable resistor /J.P./ DAC (PVR-DAC), Proceedings of the Second IEEE Asia Pacific Conference on ASICS, 2000, pp. 349-350.

*Examiner Date Considered Examiner. Initial if reference considered, whether or not citation is no conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application.

/John Poos/

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